



Mirror Technologies

Reflecting Ideas..

- ✓ **100 % OUTPUT AND QUALITY ASSURANCE**
- ✓ **100% PRACTICAL TRAINING ON ALL DOMAINS**

VLSI IEEE PROJECT TITLES LIST

DOMAIN : IEEE TRANSACTIONS ON COMMUNICATION VLSI

	CODE	PROJECT TITLES
1	VLSIC1	AN EFFICIENT VITERBI DECODER FOR DIGITAL MOBILE MULTIMEDIA BROADCASTING RECEIVER
2	VLSIC2	OPTIMIZING PILOT LOCATIONS USING FEEDBACK IN OFDM SYSTEMS
3	VLSIC3	NUMERICAL PERFORMANCE EVALUATION FOR ALAMOUTI SPACE TIME CODED OFDM UNDER RECEIVER IMPAIRMENTS
4	VLSIC4	A LOW-COMPLEXITY QOS-AWARE PROPORTIONAL FAIR MULTICARRIER SCHEDULING ALGORITHM FOR OFDM SYSTEMS
5	VLSIC5	ASYNCHRONOUS PROTOCOL CONVERTERS FOR TWO-PHASE DELAY-INSENSITIVE GLOBAL COMMUNICATION
6	VLSIC6	A PIPELINED IMPLEMENTATION OF OFDM TRANSMISSION ON RECONFIGURABLE PLATFORMS
7	VLSIC7	SCALABLE MULTIGIGABIT PATTERN MATCHING FOR PACKET INSPECTION
8	VLSIC8	FPGA IMPLEMENTATION OF SOFT INPUT VITERBI DECODER FOR CDMA2000 SYSTEM
9	VLSIC9	DESIGN OF AN FFT/IFFT PROCESSOR FOR MIMO OFDM SYSTEMS
10	VLSIC10	DESIGN OF CDMA BASED COMMUNICATION SYSTEM IN FPGA
11	VLSIC11	DIGITAL DESIGN OF DS-CDMA TRANSMITTER AND RECEIVER

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12	VLSIC12	JPEG ENCODER FOR LOW-COST FPGAS
13	VLSIC13	TWO-STAGE RADIX-4 VITERBI DECODER FOR MULTIBAND OFDM UWB SYSTEMS
14	VLSIC14	A LOW-COST AND HIGH EFFICIENCY ARCHITECTURE OF AES CRYPTO-ENGINE
15	VLSIC15	DESIGN OF ADAPTIVE VITERBI DECODER

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